

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10001478	
	Filing Date		2001-11-01	
	First Named Inventor	Craig Nemecek		
	Art Unit	2123		
	Examiner Name	Jason Scott Proctor		
	Attorney Docket Number	CD01213M		

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/J.P./	1	9532478	WO	A1	1995-11-30	Klien	Entire Document	<input type="checkbox"/>
/J.P./	2	404083405	JP	A1	1992-03-17	Yasue	Entire Document	no <input type="checkbox"/>
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Application Number	10001478
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Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

/J.P./	4	405055842	JP	A1	1993-03-26	Kondo	Entire Document	no <input type="checkbox"/>
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/J.P./	1	USPTO Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 08/03/2001; 37 pages	<input type="checkbox"/>
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/J.P./	1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
/J.P./	2	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at <a href="http://www.fapo.com/fport.htm">http://www.fapo.com/fport.htm</a> ; printed on 4/12/2005; 2 pages	<input type="checkbox"/>
/J.P./	3	ANONYMOUS, "F/Port:Fast Parallel Port for the PC" Installation Manual" Release 7.1, circa 1997, available for download from <a href="http://www.fapo.com/fport.htm">http://www.fapo.com/fport.htm</a> ; 25 pages	<input type="checkbox"/>

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/J.P./	3	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2005 from <a href="http://www.algonet.se/~staffann/developer/emulator.htm">http://www.algonet.se/~staffann/developer/emulator.htm</a> ; 6 pages	<input type="checkbox"/>
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/J.P./	9	USPTO Application Number 09/975,338 (CD00186): "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," Nemecek et al., filed on 10/10/2001; 34 pages	<input type="checkbox"/>
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	11	USPTO Application Number 09/975,338 (CD00186): "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," Nemecek et al., filed on 10/10/2001; 34 pages	<input type="checkbox"/>

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1	USPTO Requirement for Restriction/Election for Application Number 09/972,003 (CD00176) dated 05/06/2004; 4 pages	<input type="checkbox"/>
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1	USPTO Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming." Bartz et al. filed on 11/19/2001; 67 pages	<input type="checkbox"/>
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/J.P./	1	USPTO Notice of Allowance for Application Number 11/986,338 (CD1197C) dated 05/07/2009; 1 page	<input type="checkbox"/>
/J.P./	2	USPTO Application Number 11/965,291 (CD2007130): "Universal Digital Block Interconnection and Channel Routing," Snyder et al., filed on 12/27/2007; 31 pages	<input type="checkbox"/>
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/J.P./	5	USPTO Application Number 11/986,338 (CD1197C): Reconfigurable Testing System and Method," Pleis et al., filed on 11/20/2007; 41 pages	<input type="checkbox"/>
/J.P./	6	USPTO Application Number 10/172,670 (CD01242M): "Method and System for Programming a Memory Device," Snyder et al.; filed on 06/13/2002; <del>66 pages</del> <b>69 pages</b>	<input type="checkbox"/>
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/J.P./	23	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 08/02/2006; 21 pages	<input type="checkbox"/>
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/J.P./	26	USPTO Application Number 10/653,050 (CD01242C): "Method and System for Programming a Memory Device," Snyder et al.; filed on 08/29/2003; 69 pages	<input type="checkbox"/>
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/J.P./	7	USPTO Application Number 11/088,028 (CD04036): "Method and Circuit for Rapid Alignment of Signals," Moyal et al., filed on 11/13/2007; 34 pages	<input type="checkbox"/>
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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/J.P./	34	Dirk Killat, "A One-Chip Solution for Electronic Ballasts in Fluorescent Lamps," Power Electronics, < <a href="http://powerelectronics.com/mag/power_onechip_solution_electronic/">http://powerelectronics.com/mag/power_onechip_solution_electronic/</a> >, dated 03/01/2004, accessed 09/13/2005; 4 pages	<input type="checkbox"/>
/J.P./	35	Fred Eady, "PSoC 101," Circuit Cellar, August 2004, accessed 09/13/2005, < <a href="http://www.circuitcellar.com/library/print/0804/eady169/2.htm">http://www.circuitcellar.com/library/print/0804/eady169/2.htm</a> >; 4 pages	<input type="checkbox"/>
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/J.P./	37	A.F. Harvey, "DMA Fundamentals on Various PC Platforms," 1991, 1994, National Instruments Corporation, pp. 1-19; 19 pages	<input type="checkbox"/>
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/J.P./	48	Balough et al., "White Paper: Comparing IP Integration Approaches for FPGA Implementation," February 2007, Version 1.1, Altera, pp. 1-7; 7 pages	<input type="checkbox"/>

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Examiner Signature	/Jason Proctor/	Date Considered	12/18/2010
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
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Application Number	
Filing Date	
First Named Inventor	
Art Unit	
Examiner Name	
Attorney Docket Number	

/J.P./	1	USPTO Non-Final Rejection for Application Number 12/058,569 (CD2007125) dated 08/02/2010; 9 pages	<input type="checkbox"/>
/J.P./	2	USPTO Miscellaneous Action for Application Number 10/113,581 (CD01188) dated 06/23/2010; 6 pages	<input type="checkbox"/>
/J.P./	3	USPTO Non-Final Rejection for Application Number 12/104,678 (CD2007139) dated 07/02/2010; 8 pages	<input type="checkbox"/>
/J.P./	4	USPTO Final Rejection for Application Number 11/968,145 (CD2007133) dated 08/02/2010; 6 pages	<input type="checkbox"/>
/J.P./	5	USPTO Notice of Allowance for Application Number 11/985,340 (CD04036C1) dated 06/09/2010; 8 pages	<input type="checkbox"/>
/J.P./	6	USPTO Notice of Allowance for Application Number 11/850,260 (CD2007006) dated 07/02/2010; 8 pages	<input type="checkbox"/>
/J.P./	7	USPTO Notice of Allowance for Application Number 12/060,128 (CD2007143) dated 10/19/2009; 8 pages	<input type="checkbox"/>
/J.P./	8	USPTO Non-Final Rejection for Application Number 09/975,338 (CD00186M) dated 04/30/2010; 2 pages	<input type="checkbox"/>
/J.P./	9	USPTO Advisory Action for Application Number 11/818,005 (CD01171C) dated 07/30/2010; 3 pages	<input type="checkbox"/>
/J.P./	10	USPTO Non-Final Rejection for Application Number 12/132,527 (CD01175C) dated 04/29/2010; 7 pages	<input type="checkbox"/>
/J.P./	11	USPTO Advisory Action for Application Number 11/201,627 (CD04109) dated 08/05/2010; 3 pages	<input type="checkbox"/>

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Art Unit		
Examiner Name		
Attorney Docket Number		

/J.P./	12	USPTO Final Rejection for Application Number 11/201,627 (CD04109) dated 05/24/2010; 26 pages	<input type="checkbox"/>
/J.P./	13	USPTO Non-Final Rejection for Application Number 11/166,622 (CD05027) dated 06/22/2010; 11 pages	<input type="checkbox"/>
/J.P./	14	USPTO Final Rejection for Application Number 11/166,622 (CD05027) dated 03/18/2010; 12 pages	<input type="checkbox"/>
/J.P./	15	USPTO Non-Final Rejection for Application Number 11/698,660 (CD05176) dated 05/21/2010; 15 pages	<input type="checkbox"/>
/J.P./	16	USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 07/21/2010; 3 pages	<input type="checkbox"/>
/J.P./	17	USPTO Final Rejection for Application Number 11/644,100 (CD05204) dated 05/19/2010; 13 pages	<input type="checkbox"/>
/J.P./	18	USPTO Final Rejection for Application Number 11/865,672 (CD2007098) dated 12/30/2009; 6 pages	<input type="checkbox"/>
/J.P./	19	USPTO Final Rejection for Application Number 11/857,947 (CD2007116) dated 10/14/2009; 22 pages	<input type="checkbox"/>
/J.P./	20	USPTO Non-Final Rejection for Application Number 11/857,947 (CD2007116) dated 02/03/2010; 23 pages	<input type="checkbox"/>
/J.P./	21	USPTO Non-Final Rejection for Application Number 11/857,947 (CD2007116) dated 07/21/2010; 15 pages	<input type="checkbox"/>
/J.P./	22	USPTO Non-Final Rejection for Application Number 11/968,145 (CD2007133) dated 03/04/2010; 11 pages	<input type="checkbox"/>

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Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

/J.P./	1	USPTO Advisory Action for Application Number 11/779,439 (CD00167MC2) dated 03/30/2009; 3 pages	<input type="checkbox"/>
/J.P./	2	USPTO Notice of Allowance for Application Number 10/001,478 (CD01213M) dated 09/07/2010; 10 pages	<input type="checkbox"/>
/J.P./	3	USPTO Notice of Allowance for Application Number 11/818,005 (CD01171C) dated 10/26/2010; 10 pages	<input type="checkbox"/>
/J.P./	4	USPTO Final Rejection for Application Number 12/132,527 (CD01175C) dated 10/14/2010; 8 pages	<input type="checkbox"/>
/J.P./	5	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 09/30/2010; 6 pages	<input type="checkbox"/>
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/J.P./	7	USPTO Advisory Action for Application Number 11/698,660 (CD05176) dated 11/10/2010; 3 pages	<input type="checkbox"/>
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	9	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 06/11/2010; 12 pages	<input type="checkbox"/>
/J.P./	10	USPTO Final Rejection for Application Number 12/060,176 (CD2007136) dated 10/12/2010; 22 pages	<input type="checkbox"/>
/J.P./	11	USPTO Advisory Action for Application Number 12/136,577 (CD01210C) dated 10/29/2010; 3 pages	<input type="checkbox"/>

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/J.P./	12	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 12/16/2009; 13 pages	<input type="checkbox"/>
	<del>13</del>	<del>USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 07/24/2010; 3 pages</del>	<del><input type="checkbox"/></del>
/J.P./	14	USPTO Final Rejection for Application Number 11/166,622 (CD05027) dated 03/18/2010; 12 pages	<input type="checkbox"/>

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/J.P./	1	USPTO Advisory Action for Application Number 09/994,601 (CD01170) dated 03/23/2010; 2 pages	<input type="checkbox"/>
/J.P./	2	USPTO Non-Final Rejection for Application Number 11/818,005 (CD01171) dated 10/26/2010; 10 pages	<input type="checkbox"/>
/J.P./	3	USPTO Final Rejection for Application Number 12/132,527 (CD01175) dated 10/14/2010; 8 pages	<input type="checkbox"/>
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/J.P./	7	USPTO Non-Final Rejection for Application Number 09/989,767 (CD01178) dated 07/09/2010; 22 pages	<input type="checkbox"/>
/J.P./	8	USPTO Non-Final Rejection for Application Number 09/989,771 (CD01182) dated 07/20/2010; 12 pages	<input type="checkbox"/>
/J.P./	9	USPTO Advisory Action for Application Number 09/989,771 (CD01182) dated 02/03/2010; 3 pages	<input type="checkbox"/>
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/J.P./	11	USPTO Notice of Allowance for Application Number 10/113,581 (CD01188) dated 09/03/2010; 11 pages	<input type="checkbox"/>

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/J.P./	14	USPTO Final Rejection for Application Number 12/060,176 (CD07136) dated 10/12/2010; 22 pages	<input type="checkbox"/>
/J.P./	15	USPTO Notice of Allowance for Application Number 12/057,149 (CD07156) dated 11/30/2010; 7 pages	<input type="checkbox"/>

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